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DETAILED ACTION

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Reasons for Allowance

1. The following is an examiner's statement of reasons for allowance: No prior art uncovered anticipates or renders obvious applicant(s) claimed low noise voltage regulator circuit outputting the second voltage signal to the second node, wherein the switching circuit switches between a first state and a second state, when the switching circuit is at the first state, it is substantially equivalent to a voltage follower, the first voltage signal is coupled to become the second voltage signal without being filtered, when the switching circuit is at the second state, it is substantially equivalent to a RC low pass filter, the first voltage signal is filtered to become the second voltage signal; and a stabilizing circuit electrically coupled between the second node and a third node and including a negative feedback path, the stabilizing circuit receives the second voltage signal from the second node and provides the stable voltage signal to the third node.

Further, no prior art uncovered anticipates or renders obvious applicant(s) claimed low noise voltage regulator circuit having an input node and an output node, the stabilizing circuit includes a negative feedback path for providing the stable voltage signal at the output node; an intermediate module coupled between the first node and the input node, the intermediate module having a first state and the second state, wherein when the intermediate module is at the first state the first voltage signal is coupled to the input node without being filtered, and when the intermediate module is at the second state the first voltage signal is coupled to the input node after being filtered.

Any comments considered necessary by applicant must be submitted no later than the

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payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Allowable Subject Matter

1. Claims 1-13 are allowable over the prior art of record.

Conclusion

Any inquiry from other than the applicant/attorney of record concerning this communication or earlier communications from the Examiner should be directed to the Patent Electronic Business Center (EBC) at 1.866.217.9197. Any inquiry from a member of the press concerning this communication or earlier communications from the Examiner or the application should be directed to the Office of Public Affairs at 703.305.8341. Any inquiry from the applicant or an attorney of record concerning this communication or earlier communications from the Examiner should be directed to Examiner Riley whose telephone number is 571.272.2083. The Examiner can normally be reached Monday through Thursday from 7:30-6:00 p.m. Eastern Standard Time. The Examiner's Supervisor is Mike Sherry who can be reached at 571.272.2084. Any inquiry about a case's location, retrieval of a case, or receipt of an amendment into a case or information regarding sent correspondence to a case should be directed to 2800's Customer Service Center at 571.272.2815. Any inquiry of a general nature of this application should be directed to the Group receptionist whose telephone number is 571.272.2800. Status information of cases may be found at http://pair-direct.uspto.gov wherein unpublished application information is found through private PAIR and published application information is found through public PAIR. Further help on using the PAIR system is available at 1.866.217.9197 (Electronic Business Center).

Shawn Riley
Primary Examiner